Companion slides for
The Art of Multiprocessor
Programming
by Maurice Herlihy & Nir Shavit

- Today we will try to formalize our understanding of mutual exclusion
- We will also use the opportunity to show you how to argue about and prove various properties in an asynchronous concurrent setting

- Formal problem definitions
- Solutions for 2 threads
- Solutions for n threads
- Fair solutions
- Inherent costs

### Warning

- You will never use these protocols
  - Get over it
- You are advised to understand them
  - The same issues show up everywhere
  - Except hidden and more complex

# Why is Concurrent Programming so Hard?

- · Try preparing a seven-course banquet
  - By yourself
  - With one friend
  - With twenty-seven friends ...
- · Before we can talk about programs
  - Need a language
  - Describing time and concurrency

### Time

- "Absolute, true and mathematical time, of itself and from its own nature, flows equably without relation to anything external." (I. Newton, 1689)
- "Time is, like, Nature's way of making sure that everything doesn't happen all at once." (Anonymous, circa 1968)

time

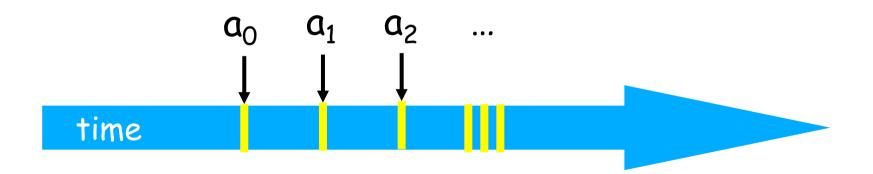
### Events

- · An event  $a_0$  of thread A is
  - Instantaneous
  - No simultaneous events (break ties)



#### Threads

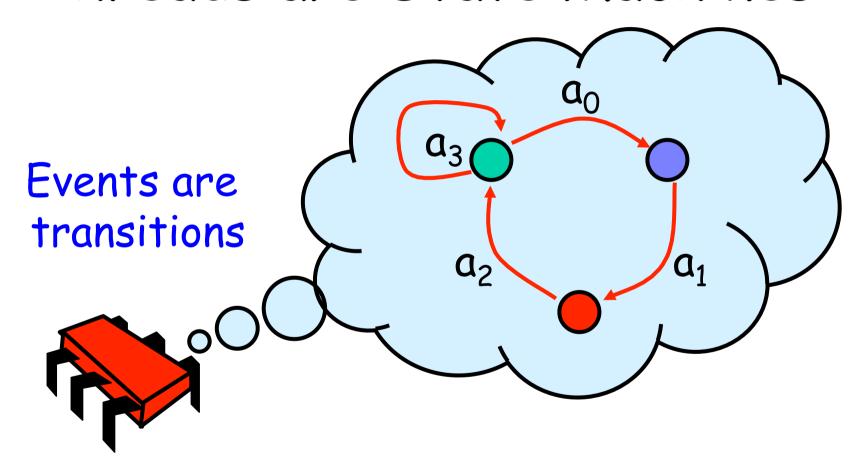
- A thread A is (formally) a sequence  $a_0, a_1, ...$  of events
  - "Trace" model
  - Notation:  $a_0 \rightarrow a_1$  indicates order



# Example Thread Events

- Assign to shared variable
- Assign to local variable
- Invoke method
- Return from method
- Lots of other things ...

### Threads are State Machines



#### States

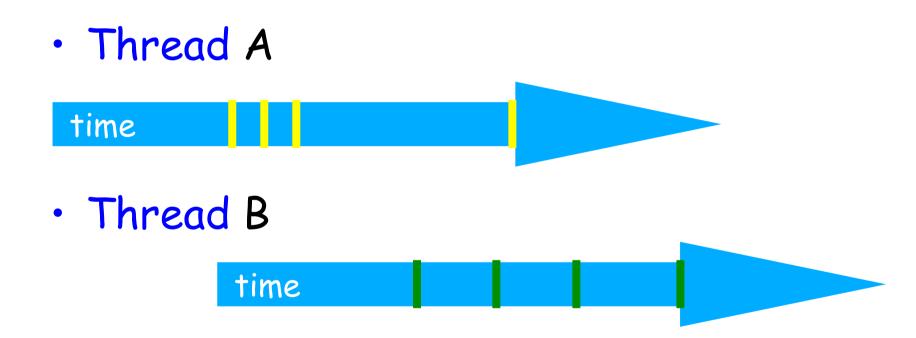
- Thread State
  - Program counter
  - Local variables
- System state
  - Object fields (shared variables)
  - Union of thread states

# Concurrency

· Thread A

time

### Concurrency



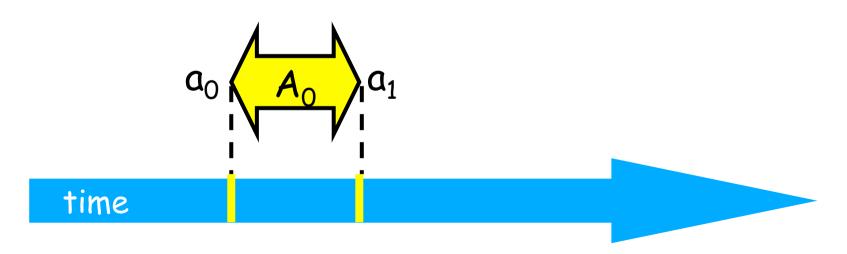
## Interleavings

- Events of two or more threads
  - Interleaved
  - Not necessarily independent (why?)

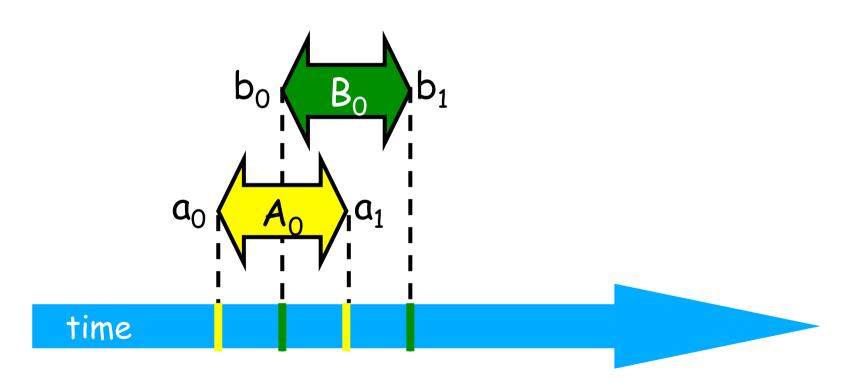
time

#### Intervals

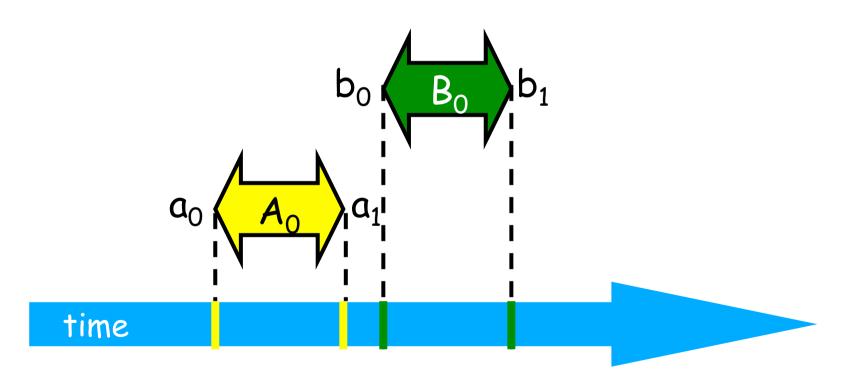
- An interval  $A_0 = (a_0, a_1)$  is
  - Time between events  $a_0$  and  $a_1$



# Intervals may Overlap

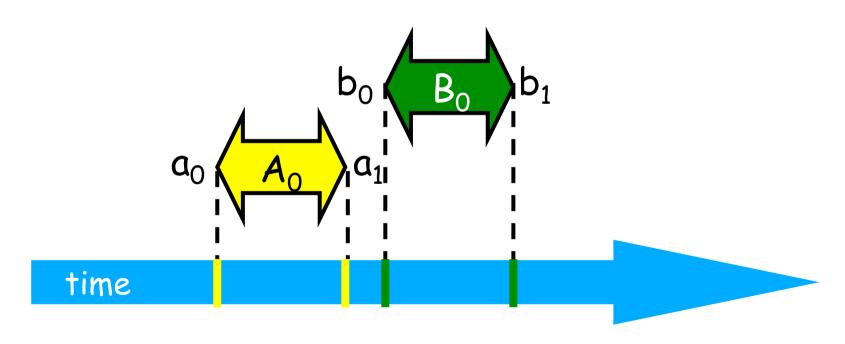


# Intervals may be Disjoint

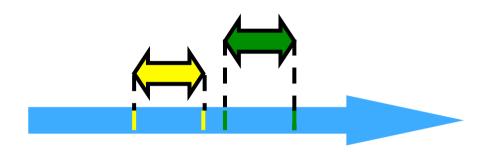


#### Precedence

#### Interval A<sub>0</sub> precedes interval B<sub>0</sub>

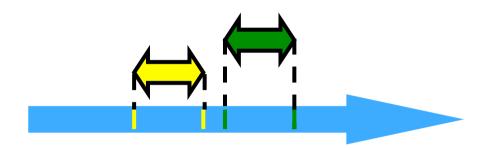


### Precedence



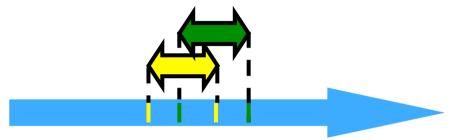
- Notation:  $A_0 \rightarrow B_0$
- · Formally,
  - End event of A<sub>0</sub> before start event of B<sub>0</sub>
  - Also called "happens before" or "precedes"

## Precedence Ordering



- Remark:  $A_0 \rightarrow B_0$  is just like saying
  - 1066 AD  $\rightarrow$  1492 AD,
  - Middle Ages → Renaissance,
- Oh wait,
  - what about this week vs this month?

# Precedence Ordering



- Never true that  $A \rightarrow A$
- If  $A \rightarrow B$  then not true that  $B \rightarrow A$
- If  $A \rightarrow B \& B \rightarrow C$  then  $A \rightarrow C$
- Funny thing: A →B & B → A might both be false!

#### Partial Orders

(you may know this already)

- · Irreflexive:
  - Never true that  $A \rightarrow A$
- · Antisymmetric:
  - If  $A \rightarrow B$  then not true that  $B \rightarrow A$
- · Transitive:
  - If  $A \rightarrow B \& B \rightarrow C$  then  $A \rightarrow C$

### Total Orders

(you may know this already)

- · Also
  - Irreflexive
  - Antisymmetric
  - Transitive
- Except that for every distinct A, B,
  - Either  $A \rightarrow B$  or  $B \rightarrow A$

### Repeated Events

```
while (mumble) {
  a_0; a_1;
                          k-th occurrence
                              of event an
                      k-th occurrence of
                       interval A_0 = (a_0, a_1)
                  Art of Multiprocessor
                                              24
                    Programming
```

## Implementing a Counter

```
public class Counter {
  private long value;
  public long getAndIncrement() {
    temp = value;
    value = temp + 1;
                          Make these steps
                           indivisible using
                                 locks
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                                             25
                   Programming
```

### Locks (Mutual Exclusion)

```
public interface Lock {
  public void lock();
  public void unlock();
}
```

### Locks (Mutual Exclusion)

```
public interface Lock {

public void lock();

public void unlock();
}
```

### Locks (Mutual Exclusion)

```
public interface Lock {

public void lock();

public void unlock();

release lock
```

```
public class Counter {
  private long value;
  private Lock lock;
  public long getAndIncrement() {
  lock.lock();
   try {
    int temp = value;
   value = value + 1;
   } finally {
     lock.unlock();
   return temp;
  }}
```

```
public class Counter {
  private long value;
  private Lock lock;
  public long getAndIncrement() {
  lock.lock();
                               acquire Lock
    int temp = value;
   value = value + 1;
   } finally {
     lock.unlock();
   return temp;
  }}
```

```
public class Counter {
  private long value;
  private Lock lock;
  public long getAndIncrement() {
  lock.lock();
  try {
    int temp = value;
    value = value + 1;
    finally {
                              Release lock
     lock.unlock();
                            (no matter what)
   return temp;
```

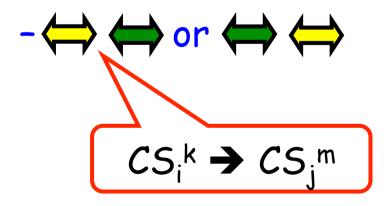
```
public class Counter {
  private long value;
  private Lock lock;
  public long getAndIncrement() {
  lock.lock();
   trv {
                                         Critical
    int temp = value;
                                         section
    value = value + 1;
   } Tinally {
     lock.unlock();
   return temp;
  }}
```

• Let  $CS_i^k \Leftrightarrow$  be thread i's k-th critical section execution

- Let  $CS_i^k \Leftrightarrow$  be thread i's k-th critical section execution
- And  $CS_j^m \Leftrightarrow$  be thread j's m-th critical section execution

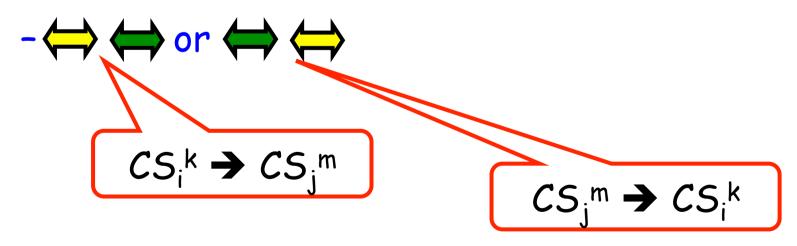
- Let  $CS_i^k \Leftrightarrow$  be thread i's k-th critical section execution
- And  $CS_j^m \iff be j's m-th execution$
- Then either
  - $\Leftrightarrow \Leftrightarrow \text{or} \Leftrightarrow \Leftrightarrow$

- Let  $CS_i^k \Leftrightarrow$  be thread i's k-th critical section execution
- And  $CS_j^m \Leftrightarrow be j's m$ -th execution
- · Then either



## Mutual Exclusion

- Let  $CS_i^k \Leftrightarrow$  be thread i's k-th critical section execution
- And  $CS_j^m \iff$  be j's m-th execution
- Then either



## Deadlock-Free

- If some thread calls lock()
  - And never returns
  - Then other threads must complete lock() and unlock() calls infinitely often
- System as a whole makes progress
  - Even if individuals starve

### Starvation-Free

- If some thread calls lock()
  - It will eventually return
- Individual threads make progress

# Two-Thread vs n-Thread Solutions

- Two-thread solutions first
  - Illustrate most basic ideas
  - Fits on one slide
- Then n-Thread solutions

#### Two-Thread Conventions

```
class ... implements Lock {
    ...
    // thread-local index, 0 or 1
    public void lock() {
        int i = ThreadID.get();
        int j = 1 - i;
    ...
}
```

#### Two-Thread Conventions

```
class ... implements Lock {
    ...
    // thread-local index, 0 or 1
    public void lock() {
        int i = ThreadID.get();
        int j = 1 - i;
        ...
    }
}
```

Henceforth: i is current thread, j is other thread

#### LockOne

#### LockOne

### LockOne

```
class LockOne implements Lock {
private volatile boolean[] flag =
                        new boolean[2];
public void lock() {
 flag[i] = true;
  while (flag[j]) {}
                            Set my flag
                      Wait for other
                      flag to go false
```

# LockOne Satisfies Mutual Exclusion

- Assume CS<sub>A</sub><sup>j</sup> overlaps CS<sub>B</sub><sup>k</sup>
- Consider each thread's last (j-th and k-th) read and write in the lock() method before entering
- Derive a contradiction

#### From the Code

- write<sub>A</sub>(flag[A]=true)  $\rightarrow$  read<sub>A</sub>(flag[B]==false)  $\rightarrow CS_A$
- write<sub>B</sub>(flag[B]=true)  $\rightarrow$  read<sub>B</sub>(flag[A]==false)  $\rightarrow$  CS<sub>B</sub>

```
class LockOne implements Lock {
...
public void lock() {
   flag[i] = true;
   while (flag[j]) {}
}
```

# From the Assumption

• read<sub>A</sub>(flag[B]==false)  $\rightarrow$  write<sub>B</sub>(flag[B]=true)

read<sub>B</sub>(flag[A]==false) → write<sub>A</sub>(flag[B]=true)

### Assumptions:

- read<sub>A</sub>(flag[B]==false)  $\rightarrow$  write<sub>B</sub>(flag[B]=true)
- read<sub>B</sub>(flag[A]==false)  $\rightarrow$  write<sub>A</sub>(flag[A]=true)

#### From the code

- write<sub>A</sub>(flag[A]=true)  $\rightarrow$  read<sub>A</sub>(flag[B]==false)
- write<sub>B</sub>(flag[B]=true)  $\rightarrow$  read<sub>B</sub>(flag[A]==false)

- Assumptions:
  - read<sub>A</sub>(flag[B]==false)  $\rightarrow$  write<sub>B</sub>(flag[B]=true)
  - read<sub>B</sub>(flag[A]==false)  $\rightarrow$  write<sub>A</sub>(flag[A]=true)
- · From the code
  - write<sub>A</sub>(flag[A]=true)  $\rightarrow$  read<sub>A</sub>(flag[B]==false)
  - write<sub>B</sub>(flag[B]=true) → read<sub>B</sub>(flag[A]==false)

- · Assumptions:
  - read<sub>A</sub>(flag[B]==false)  $\rightarrow$  write<sub>B</sub>(flag[B]=true)
  - read<sub>B</sub>(flag[A]==false) > write<sub>A</sub>(flag[A]=true)
- · From the code
  - $write_A(flag[A]=true) \rightarrow read_A(flag[B]==false)$
  - write<sub>B</sub>(flag[B]=true) → read<sub>B</sub>(flag[A]==false)

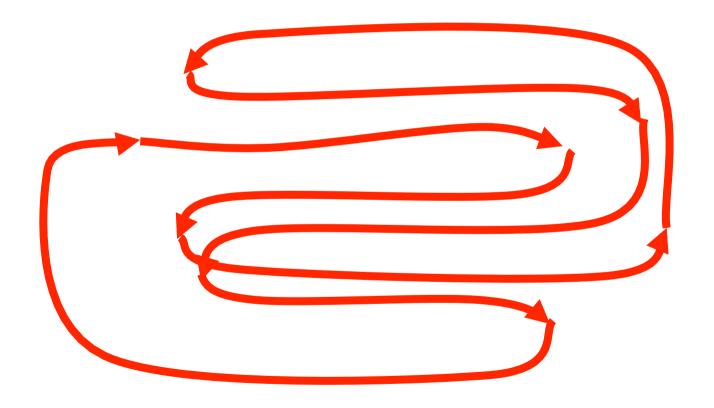
```
· Assumptions:
```

```
read<sub>A</sub>(flag[B]==false) → write<sub>B</sub>(flag[B]=true)
read<sub>B</sub>(flag[A]==false) → write<sub>A</sub>(flag[A]=true)
From the code
write<sub>A</sub>(flag[A]=true) → read<sub>A</sub>(flag[B]==false)
write<sub>B</sub>(flag[B]=true) → read<sub>B</sub>(flag[A]==false)
```

```
Assumptions
read<sub>A</sub>(flag[B]==false) → write<sub>B</sub>(flag[B]=true)
read<sub>B</sub>(flag[A]==false) → write<sub>A</sub>(flag[A]=true)
From the code
write<sub>A</sub>(flag[A]=true) → read<sub>A</sub>(flag[B]==false)
write<sub>B</sub>(flag[B]=true) → read<sub>B</sub>(flag[A]==false)
```

- · Assumptions.
  - read (flag[B] = false) write (flag[B] = true)
  - -read (flag[A] -- fulse) > write (flag[A] = true)
- · From the code
  - write (flag[A]=true) → read (flag[B]==false)
  - write<sub>B</sub>(flag[P]-true) > read<sub>B</sub>(flag[A]==false)

# Cycle!



## Deadlock Freedom

- LockOne Fails deadlock-freedom
  - Concurrent execution can deadlock

```
flag[i] = true; flag[j] = true;
while (flag[j]){} while (flag[i]){}
```

- Sequential executions OK

```
public class LockTwo implements Lock {
  private volatile int victim;
  public void lock() {
    victim = i;
    while (victim == i) {};
  }
  public void unlock() {}
}
```

```
public class LockTwo implements Lock {
  private volatile int victim;
  public void lock() {
    victim = i;
    while (victim == i) {};
  }
  public void unlock() {}
}
```

```
public class LockTwo implements
  private volatile int victim;
  public void lock() {
    victim = i;
    while (victim == i) {};
    public void unlock() {}
}
```

```
public class Lock2 implements Lock {
  private volatile int victim;
  public void lock() {
    victim = i;
    while (victim == i) {};
}

public void unlock() {}
```

## LockTwo Claims

- Satisfies mutual exclusion
  - If thread i in CS
  - Then victim == j
  - Cannot be both 0 and 1

```
public void LockTwo() {
  victim = i;
  while (victim == i) {};
}
```

- Not deadlock free
  - Sequential execution deadlocks
  - Concurrent execution does not

```
public void lock() {
  flag[i] = true;
  victim = i;
  while (flag[j] && victim == i) {};
}
public void unlock() {
  flag[i] = false;
}
```

```
public void lock() {
  flag[i] = true;
  victim = i;
  while (flag[j] && victim == i) {};
}
public void unlock() {
  flag[i] = false;
}
```

```
public void lock() {
  flag[i] = true;
    victim = i;
    while (flag[j] && victim == i) {};
}
public void unlock() {
  flag[i] = false;
}
```

```
Announce I'm
                           interested
                         Defer to other
flag[il
        = true;
victim
               && victim == i) {};
        id unlock() { Wait while other
flag[i] = false;
                        interested & I'm
                           the victim
         No longer
         interested
```

## Mutual Exclusion

```
public void lock() {
  flag[i] = true;
  victim = i;
  while (flag[j] && victim == i) {};
```

- If thread 0 in critical section,
  - flag[0]=true,
  - victim = 1

- If thread 1 in critical section,
  - flag[1]=true,
  - victim = 0

#### Cannot both be true

## Deadlock Free

```
public void lock() {
    ...
    while (flag[j] && victim == i) {};
```

- Thread blocked
  - only at while loop
  - only if it is the victim
- One or the other must not be the victim

## Starvation Free

 Thread i blocked only if j repeatedly re -enters so that

```
flag[j] == true and
victim == i
```

- When j re-enters
  - it sets victim to j.
  - So i gets in

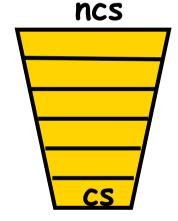
```
public void lock() {
   flag[i] = true;
   victim = i;
   while (flag[j] && victim == i) {};
}

public void unlock() {
   flag[i] = false;
}
```

# The Filter Algorithm for n Threads

There are n-1 "waiting rooms" called levels

- At each level
  - At least one enters level
  - At least one blocked if many try



· Only one thread makes it through

## Filter

```
class Filter implements Lock {
  volatile int[] level; // level[i] for thread i
  volatile int[] victim; // victim[L] for level L
 public Filter(int n) {
                                                     n-1
     level = new int[n];
                             level 00
     victim = new int[n];
     for (int i = 1; i < n; i++) {
         level[i] = 0;
     }}
                                               4
               Thread 2 at level 4
                                               n-1
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                     Programming
```

## Filter

```
class Filter implements Lock {
  public void lock(){
    for (int L = 1; L < n; L++) {
      level[i] = L;
      victim[L] = i;
      while ((\exists k != i level[k] >= L) \&\&
             victim[L] == i );
    }}
  public void unlock() {
    level[i] = 0;
  }}
```

```
class Filter implements Lock {
  44.0
    for (int L = 1; L < n; L++) {
      victim[L] = i;
      while ((\exists k != i) ]evel[k]
             victim[L] == i)
    }}
  public void release(int i) {
    level[i] = 0;
                          One level at a time
```

```
class Filter implements Lock {
  44.0
  public void lock() {
    for (int L = 1: L < n; L++) {
      level[i]
      victim[L
      while ((\exists k != 1)
                        evel[k] >= L) &&
             victim[L] ==
                                   Announce
    }}
                                  intention to
  public void release(int i)
    level[i] = 0;
                                  enter level L
```

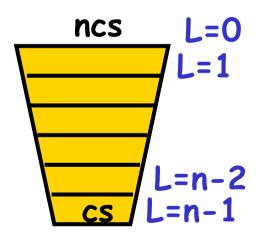
```
class Filter implements Lock {
 int level[n];
  int victim[n];
  public void lock() {
   for (int L = 1; L < n; L++) {
     level[i] = L:
     victim[L] = i;
                       level[k] >= L) &&
   }}
                              Give priority to
  public void release(int
   level[i] = 0;
                                anyone but me
```

```
Wait as long as someone else is at same or
  higher level, and I'm designated victim
 public void lock()
   for (int L = 1; L < n; L++) {
     level[i]
     victim[L] =
     while ((\exists k != i) level[k] >= L) \&\&
            victim[L] == i);
 public void release(int i) {
   level[i] = 0;
```

```
class Filter implements Lock {
 int level[n];
 int victim[n];
  public void lock() {
    for (int L = 1; L < n; L++) {
     level[i] = L;
      victim[L] = i;
     while ((\exists k != i) level[k] >= L) \&\&
             victim[L] == i);
 Thread enters level L when it completes
                   the loop
```

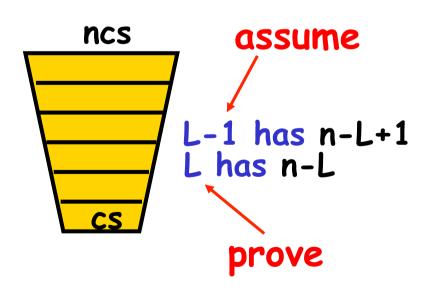
#### Claim

- Start at level L=0
- At most n-L threads enter level L
- Mutual exclusion at level L=n-1

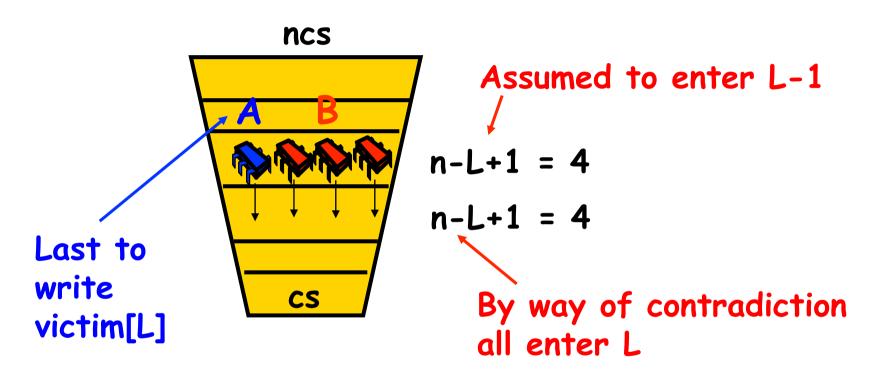


### Induction Hypothesis

- No more than n-L+1 at level L-1
- Induction step: by contradiction
- Assume all at level
   L-1 enter level
- A last to write victim[L]
- B is any other thread at level L



#### Proof Structure



Show that A must have seen

B at level L and since victim[L] == A

could not have entered

#### From the Code

(1) write<sub>B</sub>(level[B]=L) $\rightarrow$ write<sub>B</sub>(victim[L]=B)

#### From the Code

(2) write<sub>A</sub>(victim[L]=A) $\rightarrow$ read<sub>A</sub>(level[B])

### By Assumption

(3) write<sub>B</sub>(victim[L]=B) $\rightarrow$ write<sub>A</sub>(victim[L]=A)

By assumption, A is the last thread to write victim[L]

### Combining Observations

- (1) write<sub>B</sub>(level[B]=L) $\rightarrow$ write<sub>B</sub>(victim[L]=B)
- (3) write<sub>B</sub>(victim[L]=B) $\rightarrow$ write<sub>A</sub>(victim[L]=A)
- (2) write<sub>A</sub>(victim[L]=A) $\rightarrow$ read<sub>A</sub>(level[B])

#### Combining Observations

```
(1) write<sub>B</sub>(level[B]=L)\rightarrow
```

```
(3) write<sub>B</sub>(victim[L]=B)\rightarrowwrite<sub>A</sub>(victim[L]=A)
```

```
(2) \rightarrow read<sub>A</sub>(level[B])
```

#### Combining Observations

```
(1) write<sub>B</sub>(level[B]=L)→
(3) write<sub>B</sub>(victim[L]=B)→write<sub>A</sub>(victim[L]=A)
(2) → read<sub>A</sub>(level[B])
```

Thus, A read level[B] ≥ L,

A was last to write victim[L],
so it could not have entered level L!

#### No Starvation

- Filter Lock satisfies properties:
  - Just like Peterson Alg at any level
  - So no one starves
- But what about fairness?
  - Threads can be overtaken by others

### Bounded Waiting

- Want stronger fairness guarantees
- Thread not "overtaken" too much
- Need to adjust definitions ....

### Bounded Waiting

- Divide Tock() method into 2 parts:
  - Doorway interval:
    - Written D<sub>A</sub>
    - always finishes in finite steps
  - Waiting interval:
    - · Written W<sub>A</sub>
    - may take unbounded steps

- Provides First-Come-First-Served
- · How?
  - Take a "number"
  - Wait until lower numbers have been served
- · Lexicographic order
  - -(a,i) > (b,j)
    - If a > b, or a = b and i > j

```
class Bakery implements Lock {
 volatile boolean[] flag;
 volatile Label[] label;
  public Bakery (int n) {
    flag = new boolean[n];
    label = new Label[n];
    for (int i = 0; i < n; i++) {
       flag[i] = false; label[i] = 0;
```

```
class Bakery implements Lock {
  volatile boolean[] flag;
  volatile Label[] label;
  public Bakery (int n) {
    flag = new boolean[n];
    label = new Label[n];
    for (int i = 0; i < n; i++) {
       flag[i] = false; label[i] =
```

Take increasing

With lower (label,i) in lexicographic order

```
class Bakery implements Lock {
    No longer interested

public void unlock() {
    flag[i] = false;
}

labels are always increasing
```

#### No Deadlock

- There is always one thread with earliest label
- Ties are impossible (why?)

#### First-Come-First-Served

- If  $D_A \rightarrow D_B$  then A's label is earlier
  - write<sub>A</sub>(label[A]) →
     read<sub>B</sub>(label[A]) →
     write<sub>B</sub>(label[B]) →
     read<sub>B</sub>(flag[A])
- So B is locked out while flag[A] is true

- Suppose A and B in
   CS together
- Suppose A has earlier label
- When B entered, it must have seen
  - flag[A] is false, or
  - label[A] > label[B]

- Labels are strictly increasing so
- B must have seen flag[A] == false

- Labels are strictly increasing so
- B must have seen flag[A] == false
- · Labeling<sub>B</sub>  $\rightarrow$  read<sub>B</sub>(flag[A])  $\rightarrow$  write <sub>A</sub>(flag[A])  $\rightarrow$  Labeling<sub>A</sub>

- Labels are strictly increasing so
- B must have seen flag[A] == false
- · Labeling<sub>B</sub>  $\rightarrow$  read<sub>B</sub>(flag[A])  $\rightarrow$  write <sub>A</sub>(flag[A])  $\rightarrow$  Labeling<sub>A</sub>
- Which contradicts the assumption that A has an earlier label

# Bakery Y232K Bug

# Bakery Y232K Bug

# Does Overflow Actually Matter?

- · Yes
  - Y2K
  - 18 January 2038 (Unix time\_t rollover)
  - 16-bit counters
- · No
  - 64-bit counters
- Maybe
  - 32-bit counters

## ... spin locks e desempenho

material cap 7 livro Herlihy

#### Revisit Mutual Exclusion...

- Think of performance, not just correctness and progress
- Begin to understand how performance depends on our software properly utilizing the multiprocessor machine's hardware
- And get to know a collection of locking algorithms...

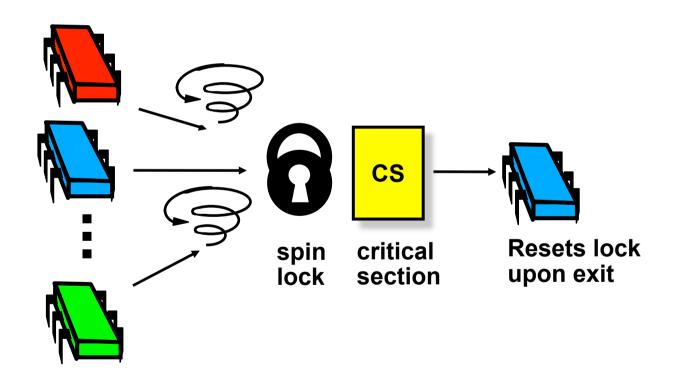
# What Should you do if you can't get a lock?

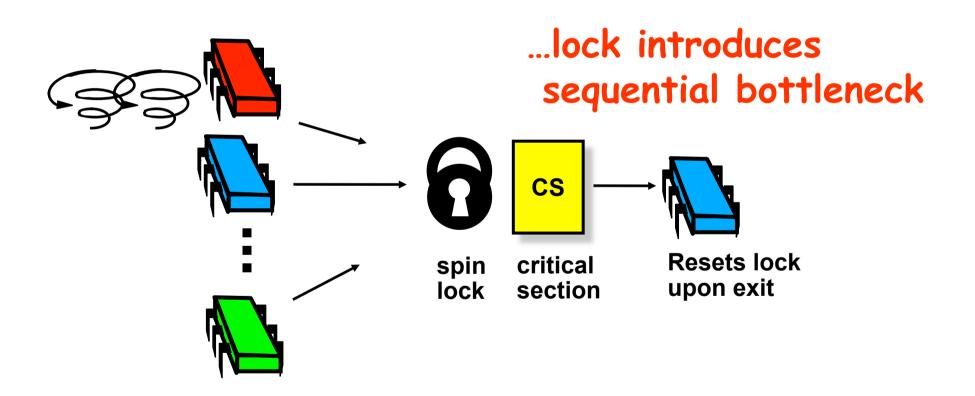
- Keep trying
  - "spin" or "busy-wait"
  - Good if delays are short
- · Give up the processor
  - Good if delays are long
  - Always good on uniprocessor

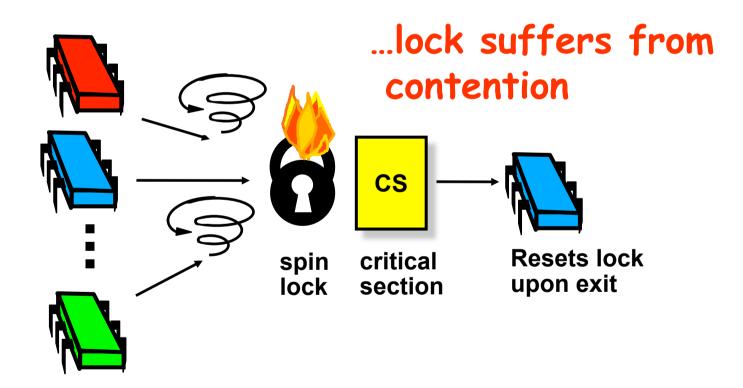
## What Should you do if you can't get a lock?

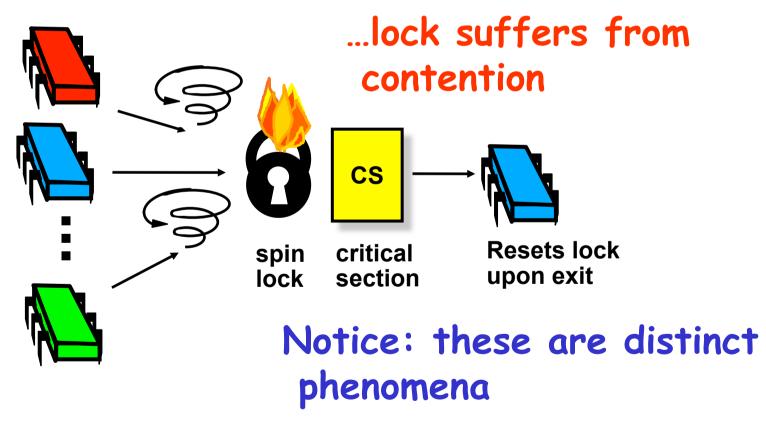
- Keep trying
   "spin" or "busy-wait"
  - Good if delays are short
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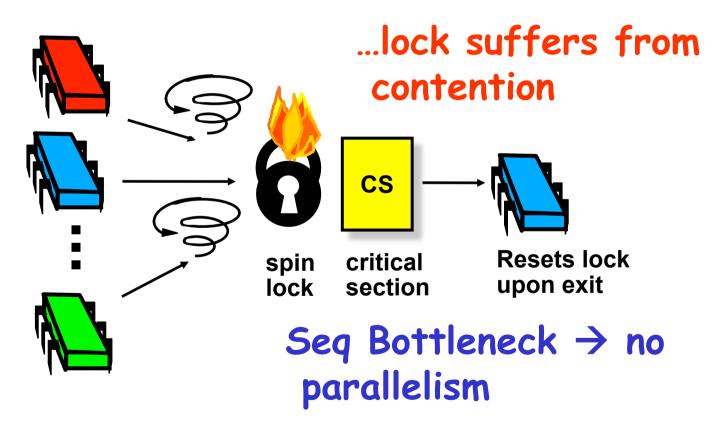
#### our focus

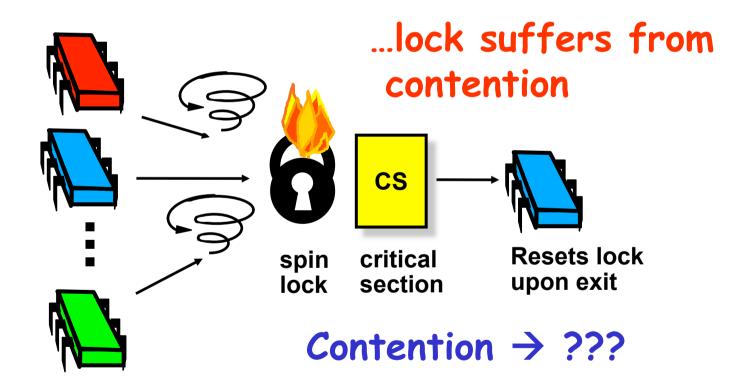












- Boolean value
- Test-and-set (TAS)
  - Swap true with current value
  - Return value tells if prior value was true or false
- Can reset just by writing false
- TAS aka "getAndSet"

```
public class AtomicBoolean {
  boolean value;

public synchronized boolean
   getAndSet(boolean newValue) {
   boolean prior = value;
   value = newValue;
   return prior;
}
```

```
public class AtomicBoolean {
  boolean value;

public synchronized boolean
  getAndSet(boolean new/alue) {
  boolean prior = value;
  value = newValue;
  return prior;
  }
   Package
  java.util.concurrent.atomic
```

```
public class AtomicBoolean {
  boolean value;

public synchronized boolean
  getAndSet(boolean newValue) {
  boolean prior = value;
  value = newValue;
  return prior;
}
```

## Swap old and new values

```
AtomicBoolean lock
= new AtomicBoolean(false)
...
boolean prior = lock.getAndSet(true)
```

AtomicBoolean lock = new AtomicBoolean(false) boolean prior = lock.getAndSet(true)

Swapping in true is called "test-and-set" or TAS

- Locking
  - Lock is free: value is false
  - Lock is taken: value is true
- Acquire lock by calling TAS
  - If result is false, you win
  - If result is true, you lose
- Release lock by writing false

```
class TASlock {
  AtomicBoolean state =
   new AtomicBoolean(false);

void lock() {
  while (state.getAndSet(true)) {}
}

void unlock() {
  state.set(false);
}}
```

```
class TASlock {
  AtomicBoolean state =
   new AtomicBoolean(false);

void lock() {
  while (state.getAndSet(true)) {}
}

void unlock() {
  state Lock state is AtomicBoolean
}}
```

```
class TASlock {
  AtomicBoolean state =
    new AtomicBoolean(false);

void lock() {
  while (state.getAndSet(true)) {}

void unlock() {
    sta Keep trying until lock acquired
}}
```

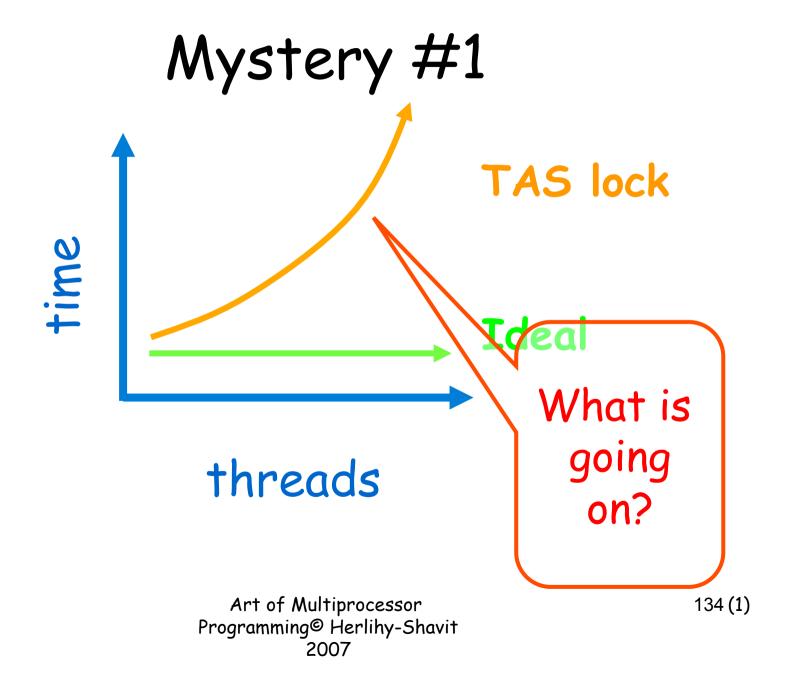
## Space Complexity

- TAS spin-lock has small "footprint"
- N thread spin-lock uses O(1) space
- As opposed to O(n) Peterson/Bakery
- How did we overcome the  $\Omega(n)$  lower bound?
- We used a RMW operation...

### Performance

- Experiment
  - n threads
  - Increment shared counter 1 million times
- How long should it take?
- How long does it take?

## Graph no speedup because of sequential time bottleneck ideal threads



#### Test-and-Test-and-Set Locks

- Lurking stage
  - Wait until lock "looks" free
  - Spin while read returns true (lock taken)
- Pouncing state
  - As soon as lock "looks" available
  - Read returns false (lock free)
  - Call TAS to acquire lock
  - If TAS loses, back to lurking

#### Test-and-test-and-set Lock

```
class TTASlock {
  AtomicBoolean state =
   new AtomicBoolean(false);

void lock() {
  while (true) {
    while (state.get()) {}
    if (!state.getAndSet(true))
      return;
  }
}
```

#### Test-and-test-and-set Lock

```
class TTASlock {
  AtomicBoolean state =
   new AtomicBoolean(false);

void lock() {
  while (true) {
    while (state.get()) {}
    if (!state.getAndSet(true))
      return;
  }
    Wait until lock looks free
```

#### Test-and-test-and-set Lock



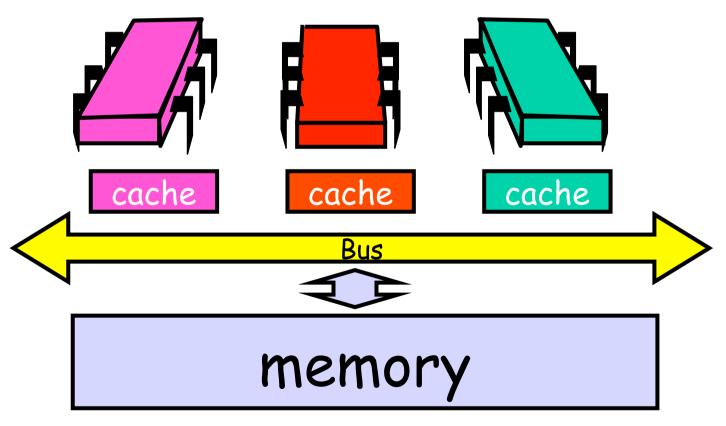
## Mystery

- Both
  - TAS and TTAS
  - Do the same thing (in our model)
- Except that
  - TTAS performs much better than TAS
  - Neither approaches ideal

## Opinion

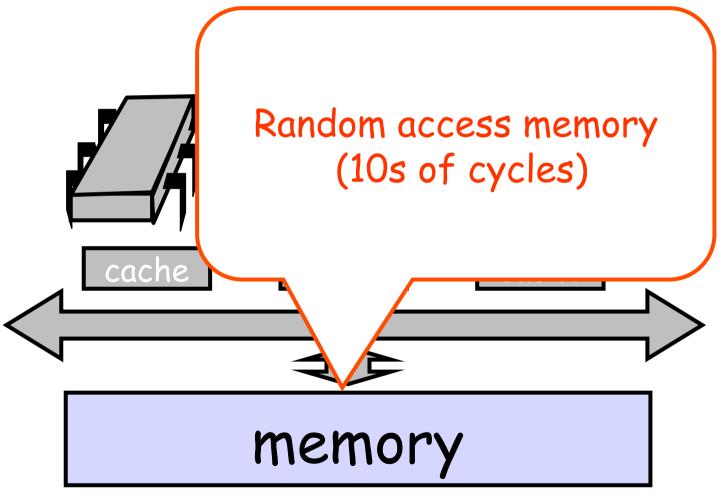
- Our memory abstraction is broken
- TAS & TTAS methods
  - Are provably the same (in our model)
  - Except they aren't (in field tests)
- · Need a more detailed model ...

#### Bus-Based Architectures



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### Bus-Based Architectures

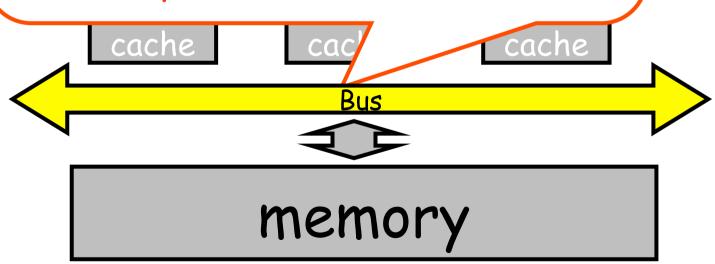


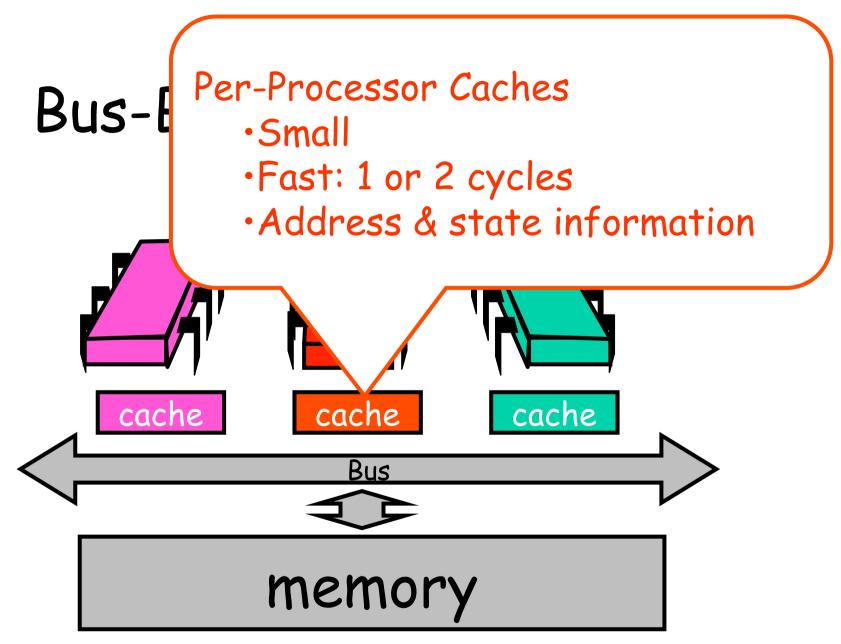
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### Ruc-Racod Architectures

#### Shared Bus

- ·broadcast medium
- ·One broadcaster at a time
- Processors and memory all "snoop"





### Jargon Watch

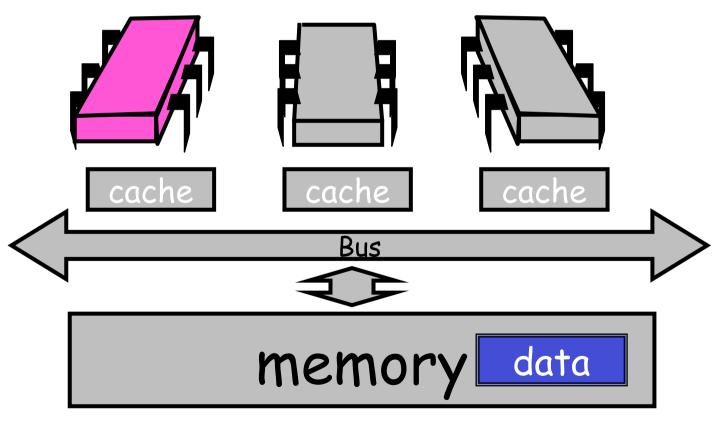
- Cache hit
  - "I found what I wanted in my cache"
  - Good Thing™

# Jargon Watch

- Cache hit
  - "I found what I wanted in my cache"
  - Good Thing™
- · Cache miss
  - "I had to shlep all the way to memory for that data"
  - Bad Thing™

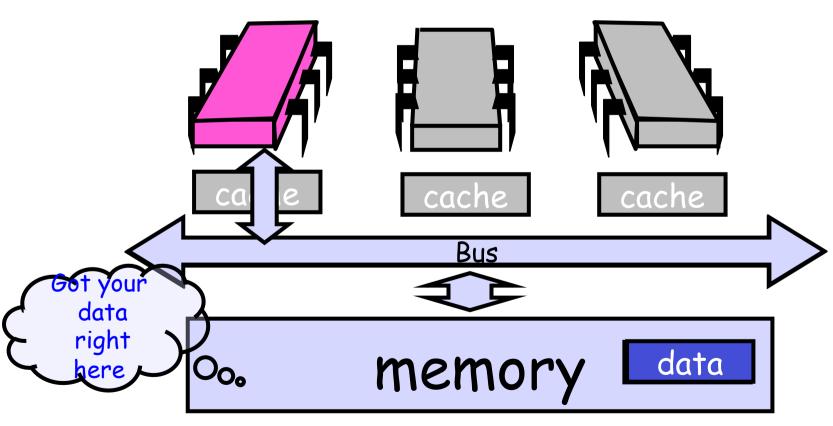
#### Cave Canem

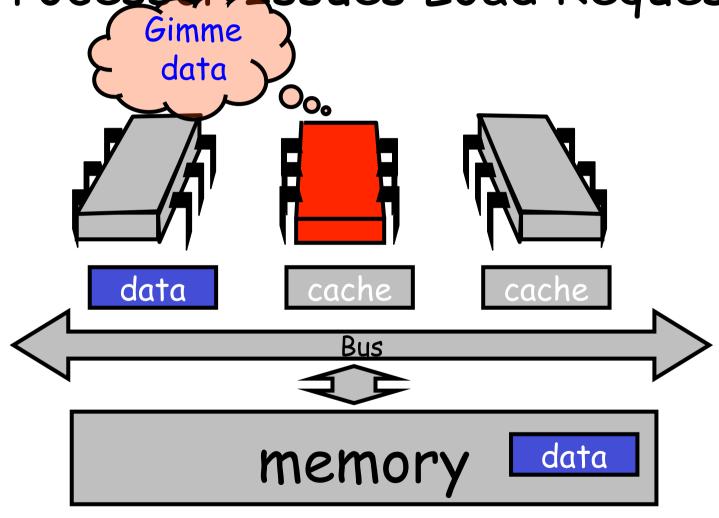
- · This model is still a simplification
  - But not in any essential way
  - Illustrates basic principles
- · Will discuss complexities later

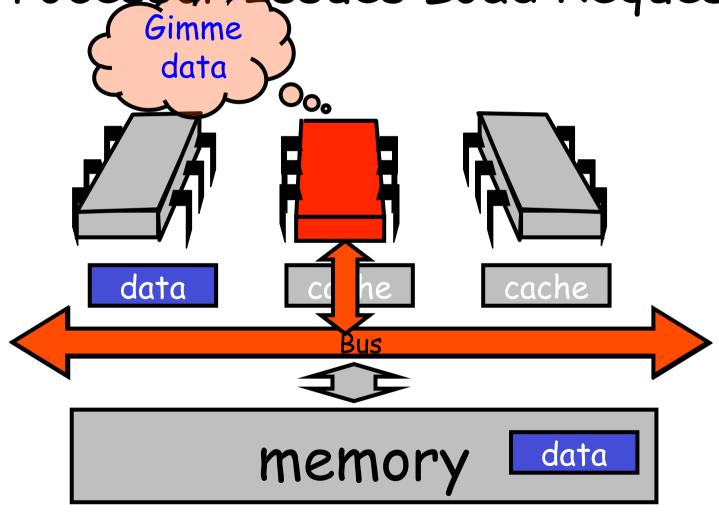


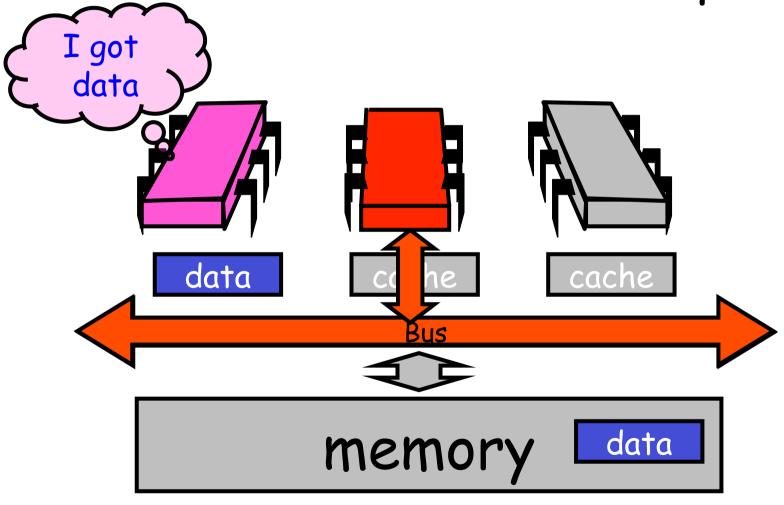
# Processor Issues Load Request Gimme data cache cache Bus data memory

# Memory Responds



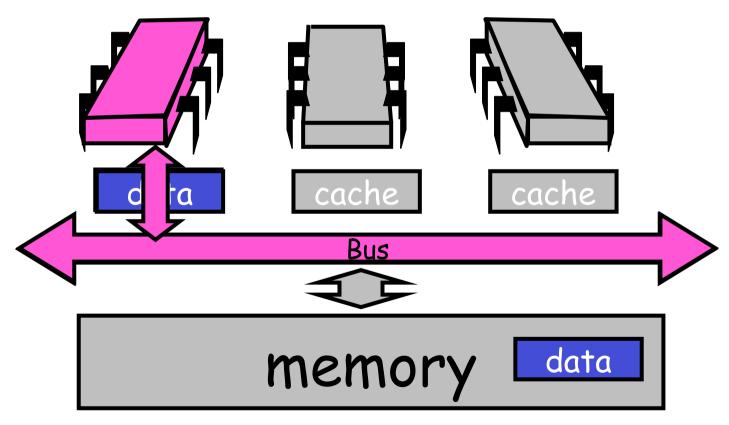


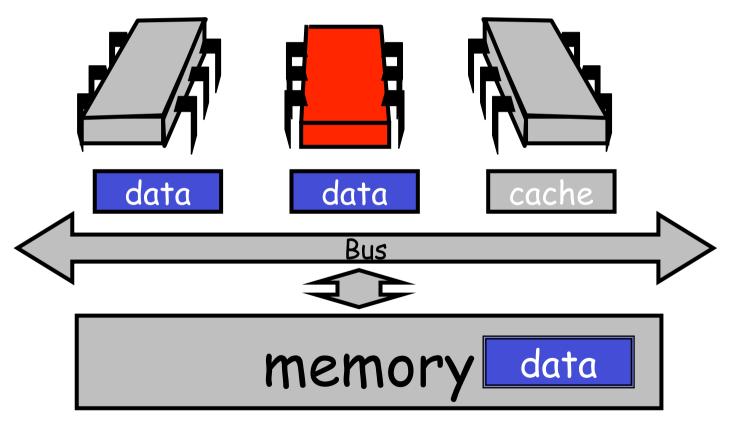


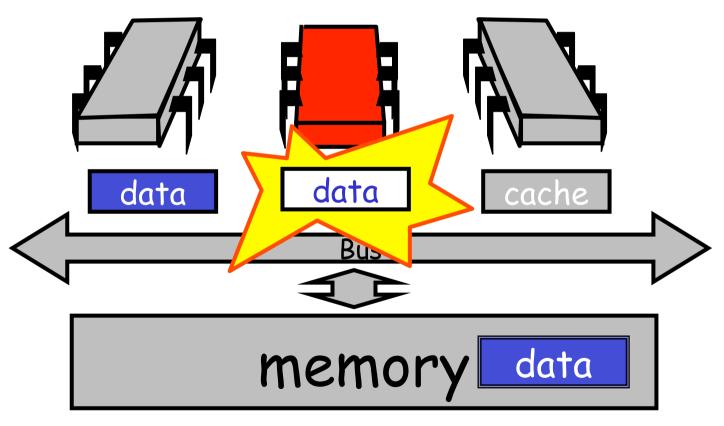


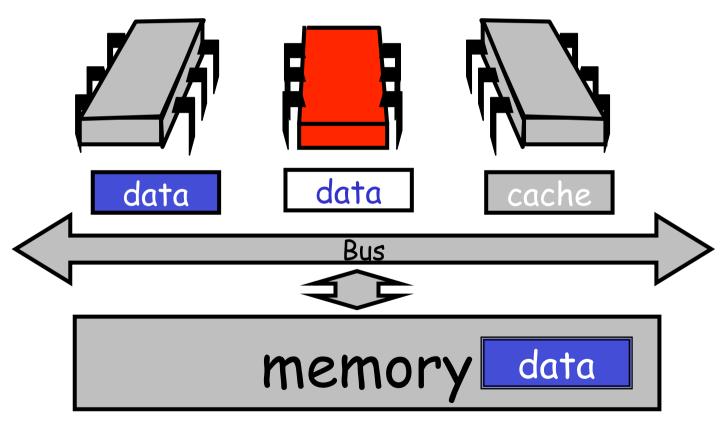
# Other Processor Responds I got data cache cache Bus data memory

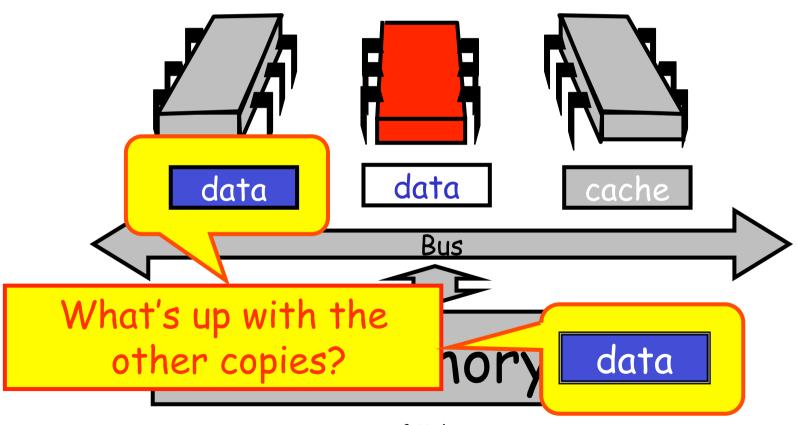
# Other Processor Responds











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#### Cache Coherence

- · We have lots of copies of data
  - Original copy in memory
  - Cached copies at processors
- · Some processor modifies its own copy
  - What do we do with the others?
  - How to avoid confusion?

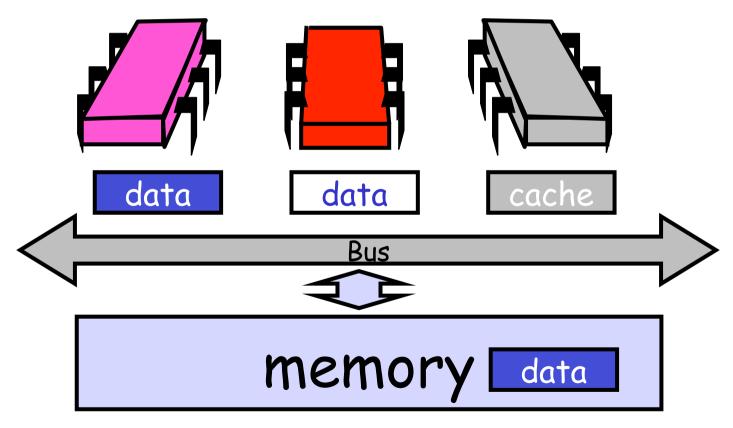
#### Write-Back Caches

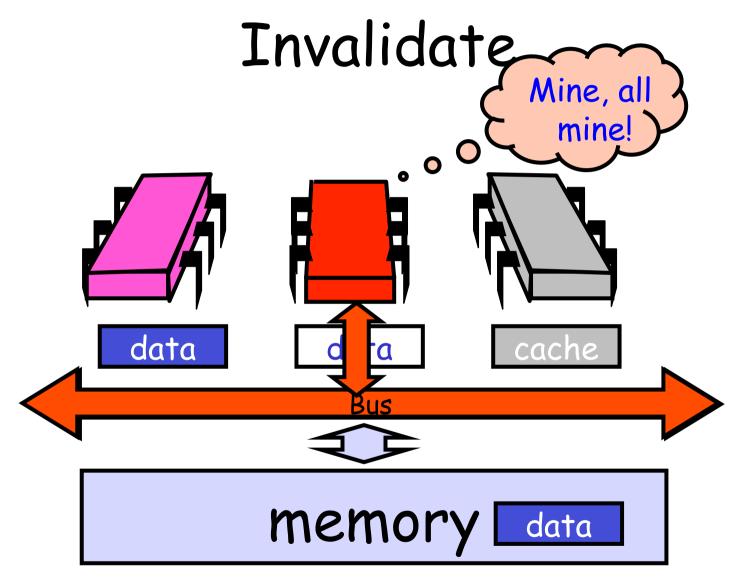
- Accumulate changes in cache
- Write back when needed
  - Need the cache for something else
  - Another processor wants it
- On first modification
  - Invalidate other entries
  - Requires non-trivial protocol ...

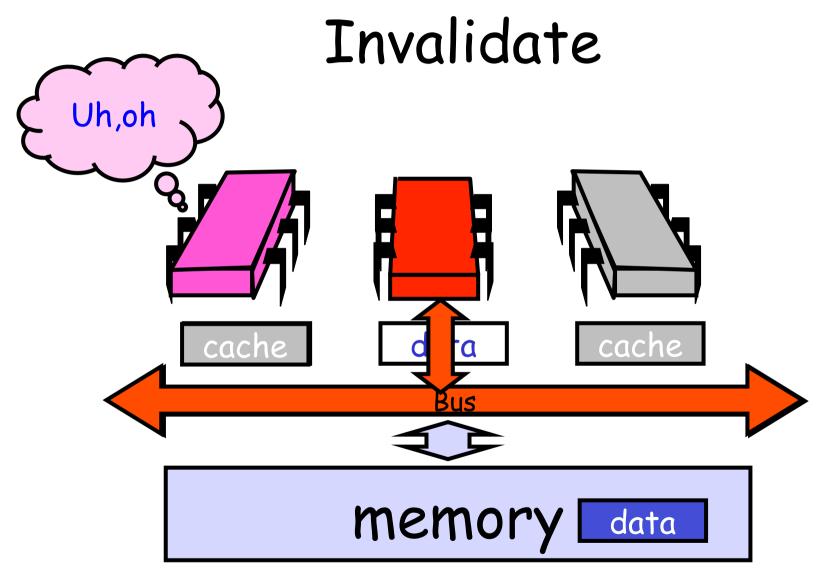
#### Write-Back Caches

- · Cache entry has three states
  - Invalid: contains raw seething bits
  - Valid: I can read but I can't write
  - Dirty: Data has been modified
    - Intercept other load requests
    - Write back to memory before using cache

#### Invalidate



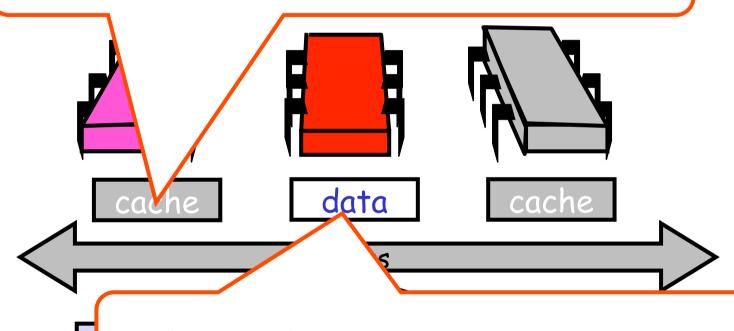




# Tryalidate Other caches lose read permission data Bus memory

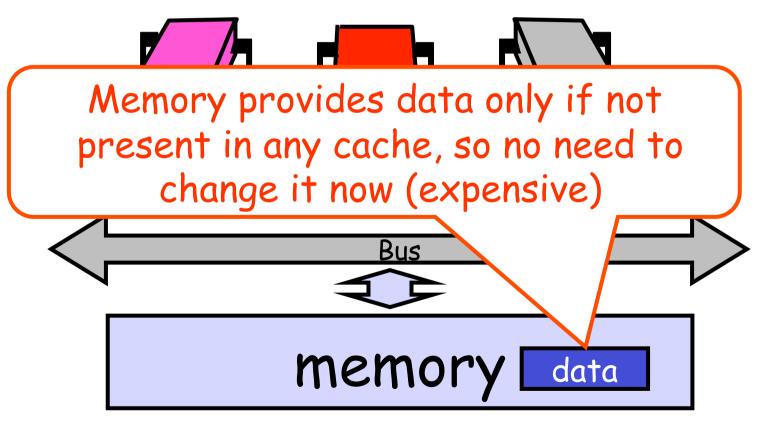
#### Tryalidate

Other caches lose read permission

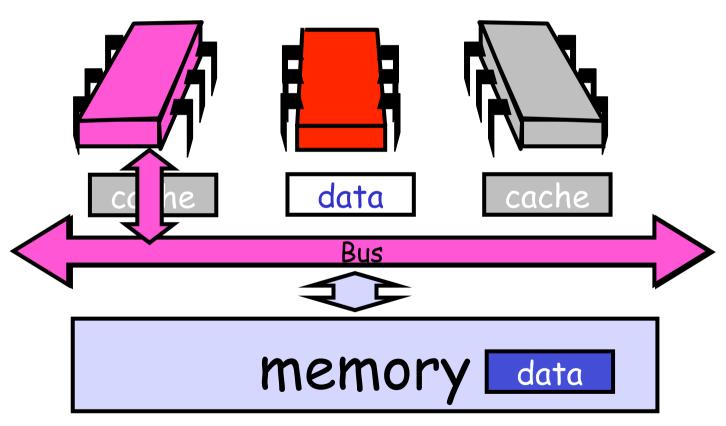


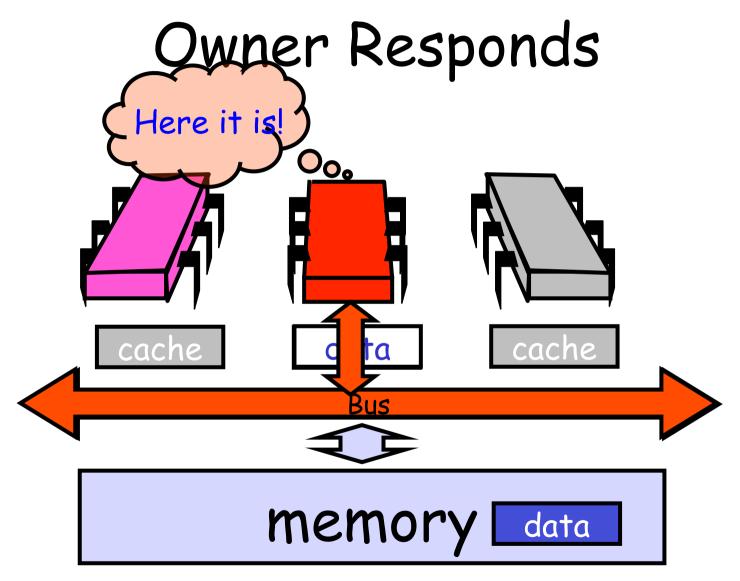
This cache acquires write permission

#### Invalidate

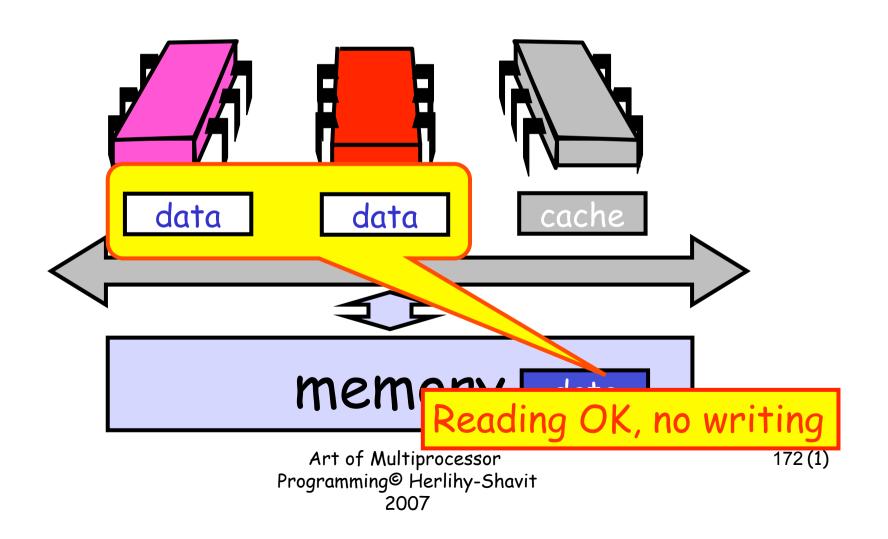


# Another Processor Asks for Data





# End of the Day ...



#### Mutual Exclusion

- What do we want to optimize?
  - Bus bandwidth used by spinning threads
  - Release/Acquire latency
  - Acquire latency for idle lock

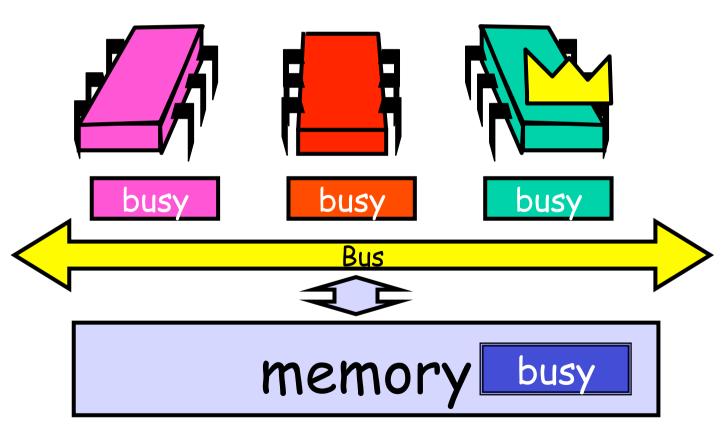
#### Simple TASLock

- TAS invalidates cache lines
- Spinners
  - Miss in cache
  - Go to bus
- Thread wants to release lock
  - delayed behind spinners

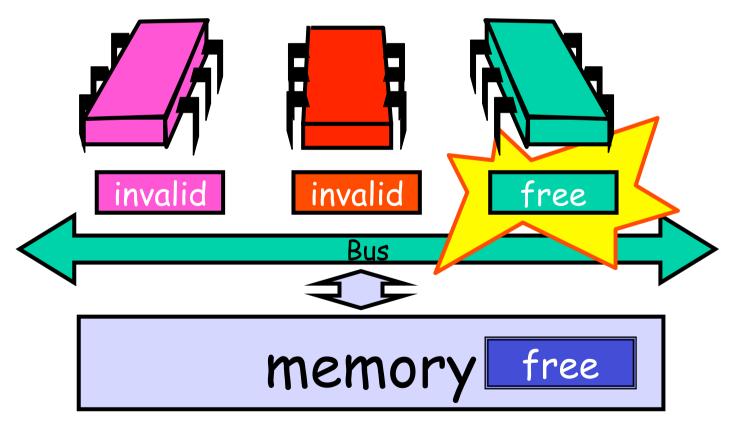
#### Test-and-test-and-set

- · Wait until lock "looks" free
  - Spin on local cache
  - No bus use while lock busy
- Problem: when lock is released
  - Invalidation storm ...

# Local Spinning while Lock is Busy

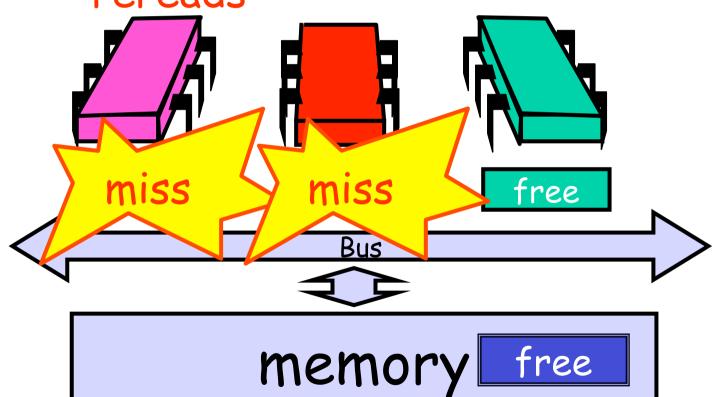


#### On Release

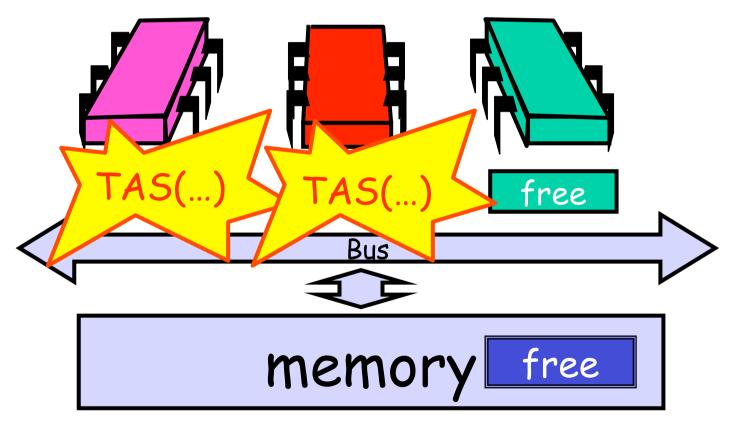


#### On Release

Everyone misses, rereads



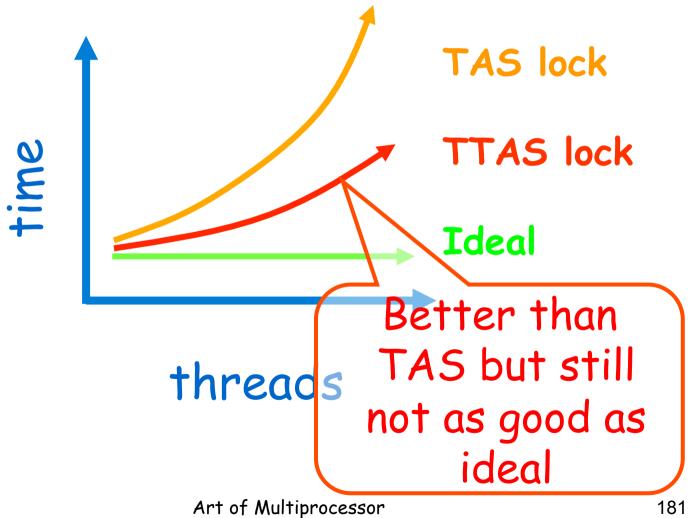
# On Release Everyone tries TAS



#### Problems

- Everyone misses
  - Reads satisfied sequentially
- Everyone does TAS
  - Invalidates others' caches
- Eventually quiesces after lock acquired
  - How long does this take?

## Mystery Explained

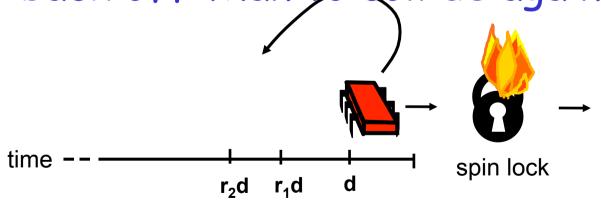


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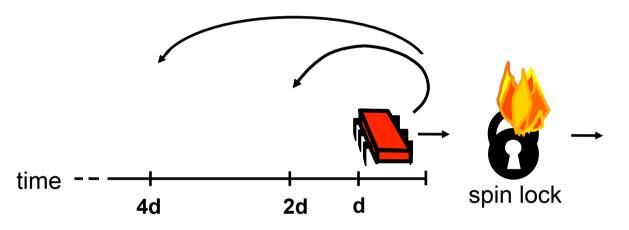
## Solution: Introduce Delay

- · If the lock looks free
  - But I fail to get it
- There must be lots of contention

· Better to back off than to collide again



# Dynamic Example: Exponential Backoff



#### If I fail to get lock

- wait random duration before retry
- Each subsequent failure doubles expected wait

```
public class Backoff implements lock {
public void lock() {
 int delay = MIN_DELAY;
 while (true) {
  while (state.get()) {}
   if (!lock.getAndSet(true))
    return;
   sleep(random() % delay);
   if (delay < MAX_DELAY)</pre>
    delay = 2 * delay;
 }}}
```

```
public class Backoff implements lock {
 public void lock()
  int delay = MIN_DELAY;
  while (true) {
   while (state.get())
   if (!lock.getAndSet(true))
    return;
   sleep(random() % delay
   if (delay < MAX_DELAY)</pre>
   delay = 2 * delay Fix minimum delay
}}}
```

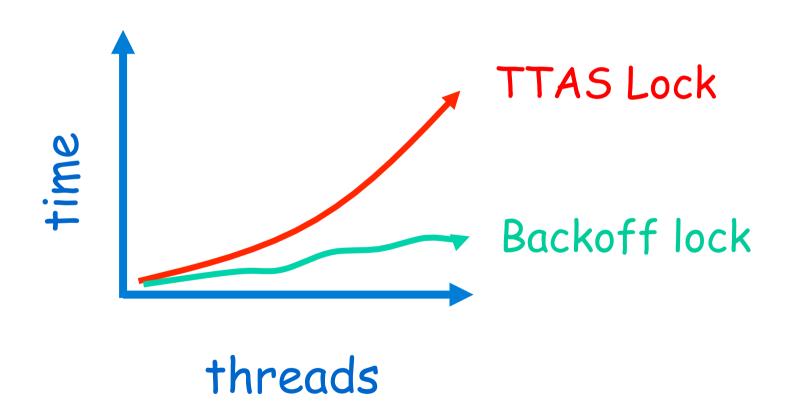
```
public class Backoff implements lock {
 public void lock() {
 int delay = MIN_DELAY;
  while (true) {
  while (state.get()) {}
   if (!lock.getAndSet(true))
    return;
   sleep(random() % dela
   if (delay < MAX_DELAY)</pre>
    delay = 2
              Wait until lock looks free
111
```

```
public class Backoff implements lock {
 public void lock() {
 int delay = MIN_DELAY;
 while (true) {
   while (state.get()) {}
  if (!lock.getAndSet(true))
    return;
   sleep(random() % delay
   if (delay < MAX_DELAY)
   delay = 2 * delay; If we win, return
}}}
```

```
public Back off for random duration
 int delay = MIN_DELAY;
 while (true) {
  while (state.get)
  if (!lock.getAndSet(true))
   return,
  sleep(random() % delay);
     (delay < MAX_DELAY
   delay = 2 * delay;
777
```

```
publ Double max delay, within reason
 int delay = MIN_DELAY;
 while (true) {
  while (state.get())
  if (!lock.getAndSet(true))
   return;
  sleep(random() % delay);
  if (delay < MAX_DELAY)
   delay = 2 * delay;
```

## Spin-Waiting Overhead



#### Backoff: Other Issues

- Good
  - Easy to implement
  - Beats TTAS lock
- Bad
  - Must choose parameters carefully
  - Not portable across platforms



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# exercícios - para 23/8

- analisar o código no slide 62 (alg Peterson) e discutir se há diferença se trocarmos as linhas 2 e 3;
- fazer um programa com threads, com pthreads+c ou Java, que tenha comportamento diferente do esperado quando se usam n threads;
- implementar um dos algoritmos de lock vistos na aula de hoje no mesmo programa.

mandar por email para noemi@inf.puc-rio.br